DOCKET NO.: MULLER 7-7 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

David A. Muller, et al.

For:

SILICON OXIDE BASED GATE DIELECTRIC LAYER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Signant of person mailing

Sir:

## **PRELIMINARY AMENDMENT**

Before examination of the above-identified application, please amend it as follows:

## **IN THE SPECIFICATION**

Please amend the specification by inserting before the first line the sentence:

--This Application is a Divisional of prior Application Serial No. 09/773,443 filed on October 2, 2003, currently pending, to David A. Muller, et al.. The above-listed Application is commonly assigned with the present invention and is incorporated herein by reference as if reproduced herein in its entirety under Rule 1.53(b).--